

DETAILED ACTION

Allowable Subject Matter

Claims 1-4 and 13-21 (renumbered as 1-13 respectively) are allowed. The following is an examiner's statement of reasons for allowance the prior art does not teach or fairly suggest in claims 1, 20, and 21:

the second node having a data conversion unit configured to receive the first DV video signal from the DV data processing unit of the first node via the IEEE1394 bus, convert the first DV video signal to a second video signal, and output the second video signal generated by conversion of the first DV video signal in the second node in synchronism with an external reference signal from the-second node, an external synchronizing signal receiver for receiving the external reference signal provided on at least one of the first and second nodes, and a synchronization adjustment unit for synchronizing a frequency of the cycle start packet output from the cycle master with a frequency of the external reference signal received by the external synchronizing signal receiver, by carrying out feedback control of a clock source frequency of the cycle master using the external reference signal, such that the transfer of the first DV video signal to the second node is synchronized with the output of the second video signal from the second node.

Further, the combination of the set forth limitations with all of the other limitations in the respective independent claims (1, 20, and 21) are not obvious. Applicant has argued this limitation on pages 7 and 8, of Applicant's Arguments filed 1-30-2012. This limitation finds support in the specification on page 7, line 24 to page 10, line 28, and Figure 1. The prior art of record neither anticipates nor renders obvious the above recited combination.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Titus Wong whose telephone number is (571) 270-1627. The examiner can normally be reached on Monday-Friday, 10am-6pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dr. Henry Tsai can be reached on (571) 272-4176. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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